

DATA DRIVEN DIGITAL SIGNAL PROCESSOR

Abstract of the Disclosure

A digital signal processor may include a plurality of processing elements that are coupled together to accomplish a specialized function. Each processing element may
5 utilize the same shared storage in a form of a plurality of general purpose registers. Each of these registers may be accessed by any of the processing elements. Each register may include a data storage section and a plurality of storage areas for data valid bits that indicate whether the
10 data is valid or not for each of the plurality of processing elements.